Notice of References Cited

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,012,157	01-2000	Lu, Bo	714/741
	В	US-5,822,228	10-1998	Irrinki et al.	714/718
	С	US-5,513,339	04-1996	Agrawal et al.	703/15
	D	US-5,475,624	12-1995	West, Burnell G.	703/15
	E	US-6,601,205	07-2003	Lehmann et al.	714/741
	F	US-4,782,488	11-1988	Anderson, Duane L.	713/190
	G	US-5,748,641	05-1998	Ohsawa, Takahashi	714/720
	Н	US-			
	ı	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			,

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q			_		
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

	HOIT ALENT BOOGNETTO						
*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	U	"SynTest Enters the BIST Product", SynTest Technolgies, Inc., March 1999, http://www.syntest.com/PressReleaseArchive/19990308_2.htm					
	٧	J. Dreibelbis et al., "Processor-Based Built-In Self-Test for Embedded DRAM", IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, November 1998, pp 1731-1740.					
	w	"Built-In Self-Test (BIST) Using Boundary Scan:, Texas Instruments Corp., December 1996, pp 1-8.					
	x	I. Burgess, "Test and Diagnosis of Embedded Memory Using BIST", Mentor Graphics Corp., September 2000, pp 1-6.					

^{*}A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.